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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(e))				Docket No. OKI.206D3	
In Re Application Of: Masahiro Yoshida et al.					
Application No. 10/762,361	Filing Date January 23, 2004	Examiner W. Lindsay, Jr.	Customer No.	Group Art Unit 2812	Confirmation No. 4915
Title: METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING A MOSFET WITH NITRIDE SIDEWALLS					
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450					
37 CFR 1.97(b)					
1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.					
37 CFR 1.97(c)					
2. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:					
<input checked="" type="checkbox"/> the statement specified in 37 CFR 1.97(e);					
OR					
<input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).					
U.S. Patent No. 4,810,666 is provided as an English language equivalent of JP 61-16571.					

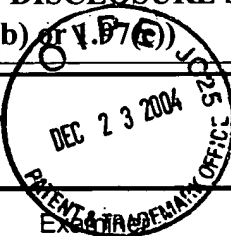
**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**

(Under 37 CFR 1.97(b) of 1.97(e))

Docket No.

OKI.206D3

In Re Application: Masahiro Yoshida et al.



Application No.

Filing Date

EXAMINER

Customer No.

Group Art Unit

Confirmation No.

10/762,361

January 23, 2004

W. Lindsay, Jr.

2812

4915

Title: **METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING A MOSFET  
WITH NITRIDE SIDEWALLS**

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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Signature

Dated: December 23, 2004

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**STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING  
INFORMATION DISCLOSURE STATEMENT**

Docket No.  
**OKI.206D3**

In Re Application Of: **Masahiro Yoshida et al.**



Application No.

**10/762,361**

Filing Date

**January 23, 2004**

Examiner

**W. Lindsay, Jr.**

Customer No.

Group Art Unit

**2812**

Confirmation No.

**4915**

Invention: **METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE INCLUDING A  
MOSFET WITH NITRIDE SIDEWALLS**

**COMMISSIONER FOR PATENTS:**

This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.

Check applicable statement herebelow:

**Statement Under 37 CFR 1.97(e)(1)**

- ☒ Each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

**Statement Under 37 CFR 1.97(e)(2)**

- ☐ No item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

*Signature*

**ANDREW J. TELESZ, JR.  
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Dated: **December 23, 2004**

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